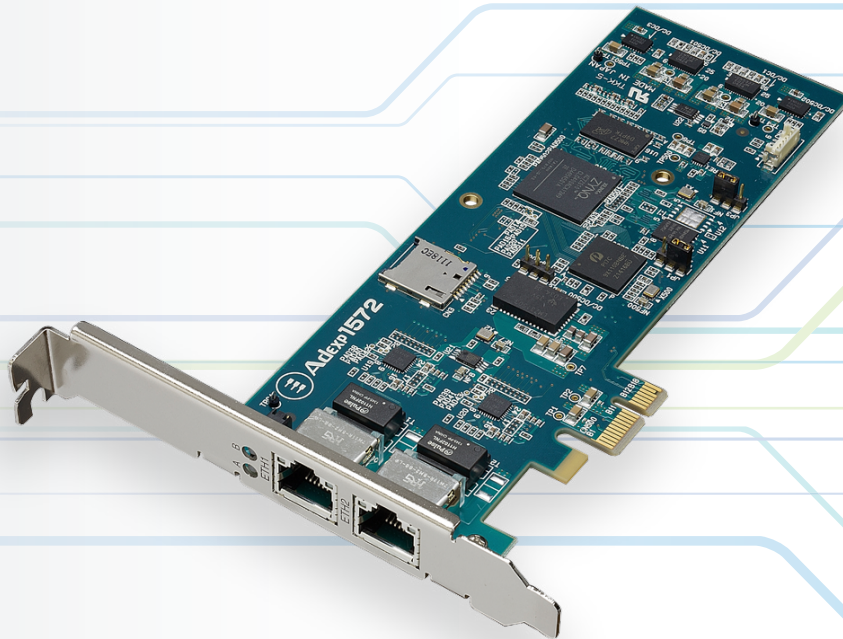


# AdEXP1572



## FEATURES

- **ARM® Cortex®-A9 Dual-Core**  
(Xilinx® Zynq®-7010)
- **EtherCAT® Master ClassA Support**
- **EtherCAT® Feature Pack Support**  
Secure Cable Redundancy / Hot Connect Capability
- **Fast Ethernet 2 Ports**
- **PCI Express Add-in Card**
- **Operating Temperature: 0°C ~ 55°C**
- **RoHS Compliant**
- **OS: Wind River® VxWorks® 6.9.x**  
**Microsoft® Windows® 7, 10**
- **Available in General Purpose Computer Base with PCI Express**
- **Available to Install to Space-Saving with Low Profile Compatible**

AdEXP1572 is a PCI Express board which has the EtherCAT® master communication function.

CPU works as an intelligent PCI Express bus slave board with Xilinx Zynq® ARM® Cortex®-A9.

As EtherCAT® master protocol stack, "EC Master" with ClassA compatible of acontis technologies is installed. Secure cable redundancy and hot connect capability are supported by EtherCAT® feature pack.

AdEXP1572 is controlled by PCI Express bus master (host CPU). The data is transferred through the shared memory which is installed in AdEXP1572.

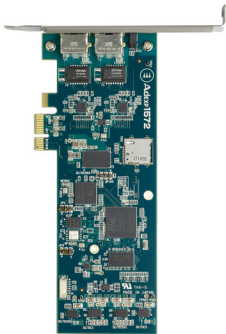
With working as an intelligent slave, AdEXP1572 realizes EtherCAT® master communication minimizing the impact for the host CPU.



## Advanet

A member of Eurotech Group

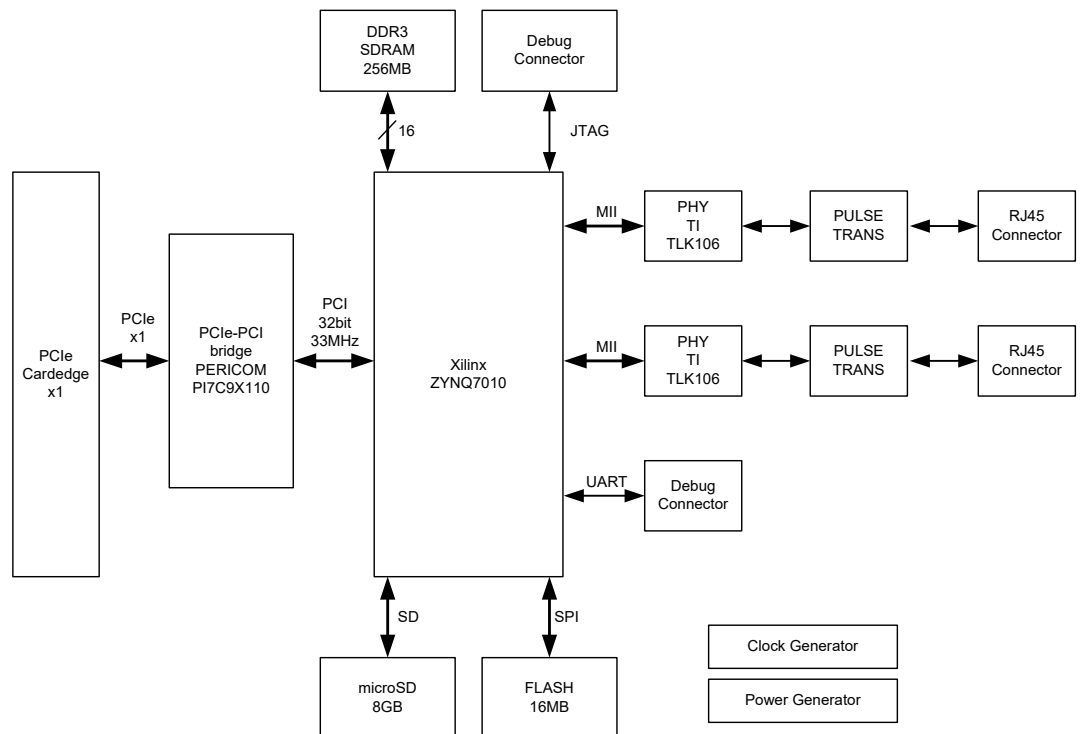
### Specifications



### Main Specification

CPU	Processor Operating Frequency	ARM® Cortex®-A9 Dual Core (Xilinx® Zynq®-7010) 666MHz
Memory	Main Memory Boot ROM 1 Boot ROM 2 Shared Memory	DDR3 SDRAM 256MB SPI-FLASH 16MB microSD (Spare) 256kB (Zynq® PL Built-in)
EtherCAT®	Classification Feature Pack Periodic Update	ClassA Compatible Secure Cable Redundancy / Hot Connect Capability Configurable at 100us ~ 10ms
Front I/O	EtherCAT®  LED	2 Ports, 100BASE-TX, RJ45 Connector (PHY: TI TLK106) *This board cannot be used as EtherCAT® communication port because it is dedicated EtherCAT®. EtherCAT® LINK / LED for ACT lamp x2 EtherCAT® Status Lamp 2 Colors LED x2
On-Board I/O	JTAG UART	1 Port for Debug 1 Port for Debug
Bus Interface	Standard  Link Width PCIe - PCI Bridge	PCI Express Base Specification Revision 1.0a PCI Express Card Electromechanical Specification Revision 1.1 X1 lane (2.5 Gbps / lane) PERICOM PI7C9X110BNBE
Power	Supply Voltage  Consumption Current	DC 12V ±8% DC 3.3V ±9% (Power Feeding from PCI Express Bus) DC 12V, 0.23A (Typ), 0.35A (Max) DC 3.3V, 0.27A (Typ), 1A (Max)
Mechanical Spec.	Dimension Weight	68.75mm x 167.50mm (Low Profile / Standard Height) 70g
Operating Env.	Operating Temp. Range Operating Hum. Range Non-Operating Temp. Range Non-Operating Hum. Range RoHS	0°C ~ 55°C 35% ~ 80% RH (Non-condensing) -10°C ~ 70°C Not greater than 90% RH (Non-condensing) Compliant
Device Driver	OS	Wind River® VxWorks® 6.9.x, Microsoft® Windows® 7, 10

### Block Diagram



Contact: sales@advanet.jp

\*Specifications are subject to change without notice.