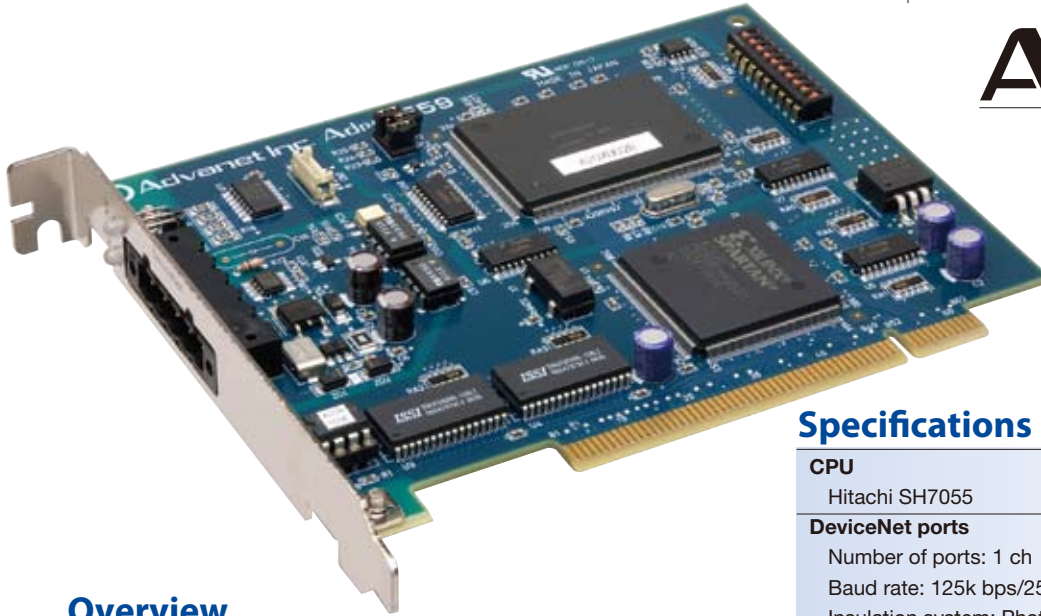


Adpci1559

DeviceNet Board



Overview

The Adpci1559 is PCI bus compatible and has DeviceNet communication functions.

This board has an SH-2E (SH7055) built in as the CPU, and it functions as an intelligent PCI bus slave board. This board is equipped with firmware with the following standard functions.

- DeviceNet master function
- DeviceNet slave function

Specifications

CPU

Hitachi SH7055

DeviceNet ports

Number of ports: 1 ch
 Baud rate: 125k bps/250k bps/500k bps
 Insulation system: Photo coupler

Bus Interface

Bus Specifications: PCI Local Bus
 Signal level: 5V TTL
 Data width: 32 bits
 Clock frequency: 33 MHz (max.)

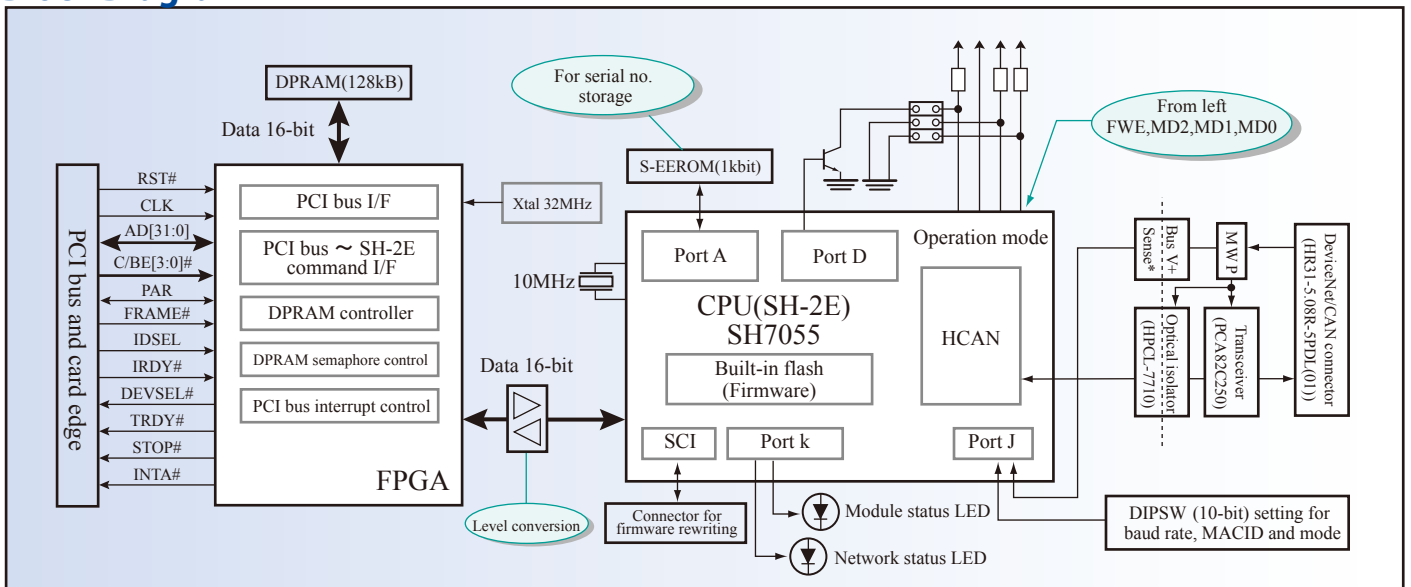
Power supply

Source voltage: DC5V ±5% (Supplied from PCI bus)
 Current consumption: 350mA (typ.) @DC5V

Dimensions

Board dimensions: 138mm × 107mm

Block Diagram



Please consult us regarding your specific custom requirements

Note: The following specifications and product appearance are subject to change for enhancement without notice.